

LIN 2.0 eVC

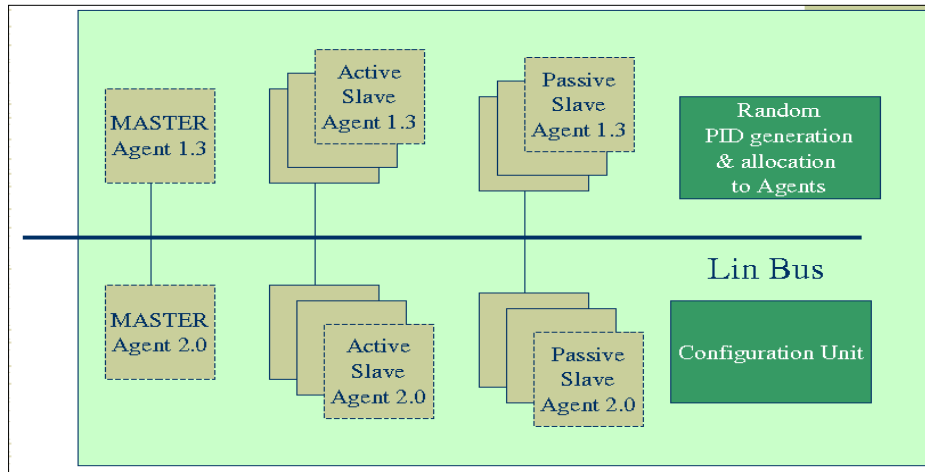
Local Interconnect Network (LIN) is a single-wire, serial communication protocol based on the UART interface that is gaining popularity as a sub-bus standard in the automotive industry. 'e' Verification Components (eVC) are reusable components that provide ready made verification environment. Tata Elxsi's LIN eVC can significantly expedite functional verification process for LIN devices across all levels of abstraction.

Features

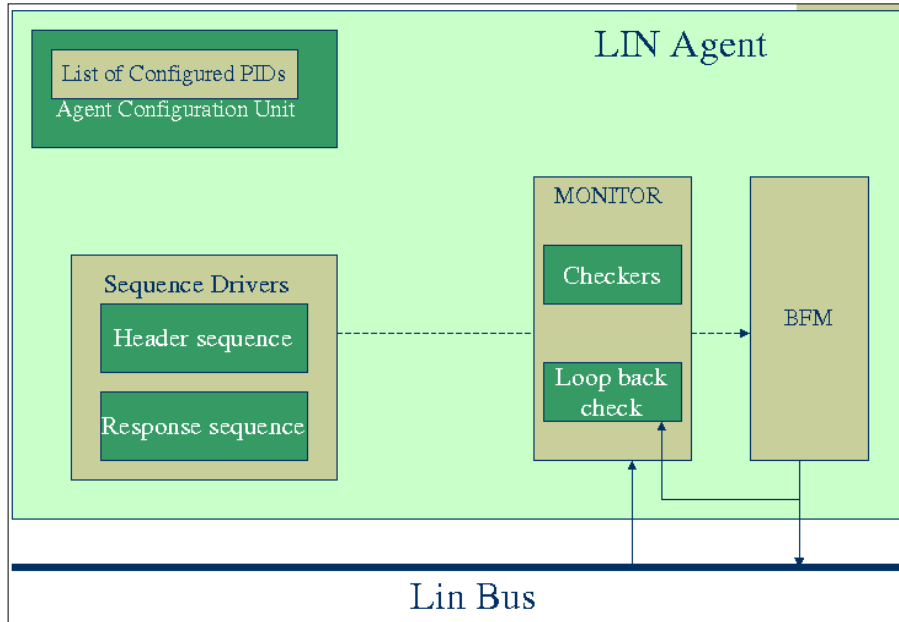
- Compatible with Local Interconnect Network (LIN) specifications version 1.3 and 2.0
- Supports Unconditional, Event-triggered and mandatory Diagnostic frames
- Simulates LIN cluster with up to 8 nodes. The user can configure the number of LIN eVC nodes.
- These LIN nodes can be configured as either Master, Active Slave or Passive Slave nodes.
- Built-in checkers ensures that LIN protocol has been followed correctly.
- The eVC supports user DUTs written in Verilog, VHDL or System-C. The DUT can either be a LIN 2.0 / 1.3 master or slave device.
- Supports injection of errors in frames.
- Allows creation of both random and directed testcases.
- Built-in coverage analysis for frames.

EVC Structure

The following diagram illustrates the architecture of Tata Elxsi LIN eVC's top level environment. It consists of several LIN nodes that can be configured as either Master or Slave.



The following diagram illustrates the main components of the eVC node (Agent).



Deliverables

- Self verified LIN eVC code in e language
- User guide & release notes
- Standalone demo script
- Sample tests for extension by users
- Coverage report from self verification

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